

2- 1-05; 4:47PM:

Application No.: 10/671,273

Docket No.: JCLA

In The Drawings

Applicant has amended FIG. 1A.

With respect to FIG. 4 about "440b", Applicant has amended specification instead.

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REMARKS**Present Status of the Application**

Applicant appreciates that the Office Action considers claims 1-7 to be allowable.

The Office Action rejected claims 35 U.S.C. 112. The Office Action objected drawings and specification. Applicant have amended specification, drawings, and claims to overcome objections and rejections. After entry of the amendments, claims 1-7 remain pending in the present application, and reconsideration of those claims is respectfully requested.

Discussion of Office Action rejections

Applicant has amended specification, drawings, and claims to overcome objections and rejections.

The Office Action has referred to paragraph [0036], in which the gate of the PMOS 408 is coupled to the voltage V_{in} . Due to the property of the PMOS, if the gate is applied to a high voltage level, the PMOS is off or non-conductive. Applicant has corrected the typographic error and improved clarity.

It is believed that the amendments have overcome the rejections.

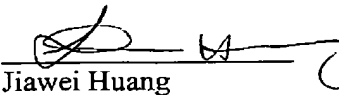
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CONCLUSION

For at least the foregoing reasons, it is believed that all the pending claims 1-7 of the invention are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Respectfully submitted,

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